

Model Implementation Conformance Statement
for the IEC 61850 interface in SEL-487E

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Testing Sub Committee

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1. Introduction

This model implementation conformance statement is applicable for SEL-487E firmware R306:

This MICS document specifies the modelling extensions compared to IEC 61850 edition 1. For the exact details on the standardized model please compare the ICD substation configuration file: "0487E 005.ICD", version R200.

Clause 2 contains the list of implemented logical nodes.
Clause 3 describes the new and extended logical nodes.

2. Logical Nodes List

The following table contains the list of logical nodes implemented in the device:

L: System Logical Nodes
LLN0 (Logical node zero)
LPHD (Physical device information)
P: Logical Nodes for protection functions
PDIF (Differential)
PHAR (Harmonic restraint)
PIOC (Instantaneous overcurrent)
PTOC (Time overcurrent)
PTOV (Overvoltage)
PTRC (Protection trip conditioning)
PTUV (Undervoltage)
PVPH (Volts per Hz)
R: Logical nodes for protection related functions
RBRF (Breaker failure)
RDRE (Disturbance recorder function)
G: Logical Nodes for generic references
GGIO (Generic process I/O)
M: Logical Nodes for metering and measurement
MMXN (Non phase related Measurement)
MMXU (Measurement)
MSQI (Sequence and imbalance)
C: Logical Nodes for control
CSWI (Switch controller)
X: Logical Nodes for switchgear
XCBR (Circuit breaker)
XSWI (Circuit switch)